



PTO/SB/08A (10-01)

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 4

<i>Complete If Known</i>		
Application / Conf. No.	10/783,216	/ 6678
Filing Date	February 20, 2004	
First Named Inventor	Tim Tuan	
Art Unit	2825	
Examiner Name	Nelson G. Lant GARBOWSKI	
Attorney Docket Number	X-1462-1P US	

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number	Publication Date MM-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Kind Code (if known)			
		US- 5,362,989	11-1994	Hennedy	
		US- 5,519,663	05-1996	Harper et al.	
		US- 5,583,457 A	12-1998	Horiguchi et al.	
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		US- 6,384,626	05-2002	Tsai et al.	
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		US- 6,489,804	12-2002	Burr, James B.	
		US- 6,583,645	06-2003	Bennett et al.	
		US- 6,710,621	03-2004	Devlin et al.	
		US- 6,711,719	03-2004	Cohn et al.	
		US- 6,747,478	06-2004	Madurawe, Raminda U.	
		US- 6,839,888	01-2005	Gupta, Vidyabhusan	
		US- 6,936,917	08-2005	Lopata et al.	

FOREIGN PATENT DOCUMENTS

**Examiner
Signature**

A detailed line drawing of a long, slender, slightly curved insect larva or caterpillar, possibly a silkworm, shown from a side perspective. The body is elongated with distinct segments, and it features a prominent, slightly hooked head at one end.

Date Considered

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 801.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 18 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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		Art Unit	2825
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		Number : Kind Code (if known)			
		US- 6,950,998	09-2005	Tuan, Tim	
		US- 6,960,934	11-2005	New, Bernard J.	
		US- 6,981,160	12-2005	Thaker et al.	
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		US- 7,170,315	01-2007	Bakker et al.	
		US- 2003/0218478	11-2003	Sani et al.	
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		US- 2005/0201174	09-2005	Klein, Dean A.	
		US- 2006/0053246	03-2006	Lec, Schweiray Joseph	
		US- 2006/0069851	03-2006	Chung et al.	
		US- 2006/0202713	09-2006	Sergey Shumarayev, San	
		US- 2007/0001720	01-2007	Li et al.	
		US-			

FOREIGN PATENT DOCUMENTS

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Signature**

A handwritten signature in black ink, appearing to read "James C. H. Smith".

Date Considered

9131

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet

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of

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Complete if Known

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First Named Inventor	Tim Tuan		
Art Unit	2825		
Examiner Name	Nelson C. Lam GARBOWSKI		
Attorney Docket Number	X-1462-1P US		

OTHER – NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
W		ALLEN/HOLBERG; Chapter 10; "Bandgap Voltage Reference"; April 12, 2000; downloaded on January 3, 2006 from www.ece.utexas.edu/~holberg/lecture_notes/bandgap.pdf ; pp. 1-5.	
		MICROCHIP TECHNOLOGY INC.; "Micropower Voltage Supervisors"; MCP102/103/121/131; Copyright 2005; downloaded on January 3, 2006 from www.microchip.com/downloads/en/DeviceDoc/21906b.pdf ; pp. 1-28	
		XILINX, INC.; "Virtex-II Pro Platform FPGA Handbook"; published October 14, 2002; available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; pp. 19-71.	
		XILINX, INC., "Spartan-3L Low Power FPGA Family", Preliminary Product Specification, DS313, September 15, 2005, v1.1, pages 1-10, available from Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124-3L	
		INTEL CORPORATION, "Intel PXA27x Processor Family Power Requirements", Application Note 2004, pages 1-36, available from Intel Corporation (Santa Clara) Corporate Office, 2200 Mission College Blvd., Santa Clara, California 95052-8119.	
		NOWKA, KEVIN J., A32-bit PowerPC System-on-a-Chip With Support For Dynamic Voltage Scaling and Dynamic Frequency Scaling", November 2002, pp. 1441-1447, Vol. 37, No. 11, IEEE Journal of Solid-State Circuits, Available from IEEE; 3 Park Avenue, 17th Floor, New York, NY 10016-5997.	
		TEXAS INSTRUMENTS (BENCHMARQ) - Datasheet BQ4011 (32x8 nonvolatile SRAM) 08/93 Pgs 1-11.	
		TEXAS INSTRUMENTS - Datasheet BQ4011 (32x8 nonvolatile SRAM) 05/99 Pgs 1-15.	
		10/971,934 filed 10/22/04, JENKINS, JESSE H. IV, entitled "Low Power Zones For Programmable Logic Devices", Xilinx, Inc., San Jose, CA	
		11/268,265 filed 11/04/05, RAHMAN, ARIFUR, et al. entitled "Implementation of Low Power Standby Modes For Integrated Circuits", Xilinx, Inc., San Jose, CA	
		11/325,888 filed 01/04/06, TUAN, TIM, entitled "Programmable Low Power Modes For Embedded Memory Blocks", Xilinx Inc., San Jose, CA	

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Date Considered

9/13/07

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Sheet 4 of 4

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		Filing Date	February 20, 2004
		First Named Inventor	Tim Tuan
		Art Unit	2825
		Examiner Name	Nelson C. GARBOWSKI
Sheet	4	of	4
		Attorney Docket Number	X-1462-1P US

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<i>W</i>		11/326,542 filed 01/04/06, JACOBSON, NEIL G., et al., entitled "Method and Mechanism for Controlling Power Consumption of an Integrated Circuit", Xilinx, Inc., San Jose, CA	
		11/329,646 filed 01/11/06, JENKINS, JESSE H. IV, entitled "Programmable Detection of Power Failure in an Integrated Circuit", Xilinx, Inc., San Jose, CA	
		11/502,939 filed 08/11/06, TUAN, TIM, et al., entitled "Disabled Unused/Inactive Resources in Programmable Logic Devices for Static Power Reduction", Xilinx, Inc., San Jose, CA	

**Examiner
Signature**

A diagram showing a closed loop formed by a thick black line. The loop has several irregular, wavy segments. A single, thin dashed vertical line segment is positioned within the loop, extending from the bottom left towards the top right.

Date Considered

9 | 3 | -

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